

Form PTO-1449 (modified) LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT(S)' INFORMATION DISCLOSURE STATEMENT	Attorney Docket No. Intel 2207/618602	Parent Serial No. 09/224,377
	Applicant Per HAMMARLUND et al.	
	Parent Filing Date December 31, 1998	Group Art Unit 2171

U. S. PATENT DOCUMENTS

EXAMINER INITIAL	PATENT NUMBER	PATENT DATE	NAME	CLASS	SUBCLASS	FILING DATE
<i>JGC</i>	5,829,051	October 27, 1998	Steely, Jr. et al.	711	216	April 4, 1994
	5,832,534	November 3, 1998	Singh et al.	711	141	October 31, 1995
	5,845,331	December 1, 1998	Carter et al.	711	163	September 28, 1994
	5,903,908	May 11, 1999	Singh et al.	711	122	October 15, 1996
↓	6,105,111	August 15, 2000	Hammarlund et al.	711	136	March 31, 1998

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS

EXAMINER INITIAL	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.
<i>JGC</i>	Peter Song, "Multithreading Comes of Age - Multithread Processors Can Boost Throughput on Servers, Media Processors", dated July 14, 1997, pp. 13-18
	Dean M. Tullsen, Susan J. Eggers, Joel S. Emer, Henry M. Levy, Jack L. Lo, and Rebecca L. Stamm, Proceedings from the 23rd Annual International Symposium on Computer Architecture, "Exploiting Choice: Instruction Fetch and Issue on an Implementable Simultaneous Multithreading Processor", dated May 22-24, 1996, pp. 191-202
	Richard J. Eickemeyer, Ross E. Johnson, Steven R. Kunkel, Mark S. Squillante and Shiafun Liu, Proceedings from the 23rd Annual International Symposium on Computer Architecture, "Evaluation of Multithread Uniprocessors for Commercial Application Environments", dated May 22-24, 1996, pp. 203-212
	Dennis Lee, Jean-Loup Baer, Brad Calder and Dick Grunwald, "Instruction Cache Fetch Policies for Speculative Execution", pp. 1-11 <i>no date</i>
	James Laudon, Anoop Gupta, and Mark Horowitz, Multithreaded Computer Architecture: A Summary of the State of the Art, "Architectural and Implementation Tradeoffs in the Design of Multiple-Context Processors", pp. 166-200, <i>no date</i>
↓	Simon W. Moore, University of Cambridge, England, "Multithreaded Processor Design", copyright 1996, pp. 1-141

EXAMINER <i>Ye Lu Chen</i>	DATE CONSIDERED <i>8/4/04</i>
EXAMINER: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	